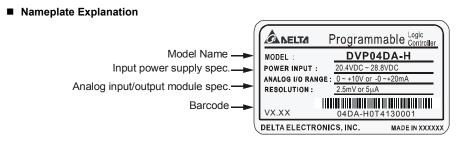


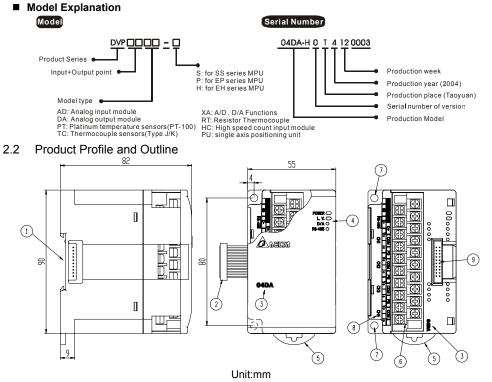


1. DIN rail track (35mm)

Analog Input Module **Instruction Sheet** WARNING

2.1 Model Explanation and Peripherals





| | 2004-12-16 | 1. DIN rail track (35mm) 6. Terminals | | | | | | RS-485 | | alog Ou | utput Modu |
|-----|--|--|--|--------------------------------|---|--|------------|---------------------|----------|---------------------------|---------------------------------|
| | | 2. Mounting hole for wire to connect extension unit/outpaging module 7. Mounting hole | | | | | | Paramete Address | ers Late | ched | Register Na |
| | | | unit/extension module 7. Would hing hole 3. Model name 8. Terminal layout | | | | | | | R/W C | CH1 output va |
| | | | | | | o connect extension | #6 #7 | H 4038 H 4039 |) () | R/W CI | CH2 output va |
| htt | p://www.delta.com.tw/products/plc.asp 5011625201-4DE1 | 4 | Indicator for power, error | r and run status | unit/extension | | #8 #9 | H 403A H 403B | | | CH3 output va CH4 output va |
| | DVP04DA-H | ť | 5. DIN rail clip | | | | | н 4036)~#17 | | R/W CI | |
| | DVP-EH Analog Input Module | 2.3 | External wiring | | _ | | #18 | H 4044 | 0 | va | o adjust OFF alue of CH1 |
| | | | voltage output | | Note 1: Please iso | plate analog output and other | #19 | H 4045 | - | va | o adjust OFF alue of CH2 |
| | Instruction Sheet | _ | *2 0V~+10V CH1 | СН1 | power wir | ing. | #20 | H 4046 | 0 | | o adjust OFF alue of CH3 |
| 1 | WARNING | L | | | Note 2: If the nois | e interference from loaded wiring | #21 | H 4047 | \circ | | o adjust OFF alue of CH4 |
| A | Please carefully read this instruction thoroughly prior to use the DVP04DA-H. | AC driv scale va | e, recorder, 🛓 shielding cable *1 FG | # 7 | input term | ninal of is significant, please | | 2 ~ #23 | | | |
| Â | | | current output | | connect a | α capacitor with 0.1~0.47μF 25V | #24 | H 404A | . 0 | | o adjust GAII alue of CH1 |
| | This is an OPEN-TYPE built-in DVP04DA-H, and the DVP04DA-H is certified to meet the safety | | 0mA~20mA CH2 V+ | CH2 | for noise | filtering. | #25 | H 404B | 0 | | o adjust GAI |
| | requirements of IEC 61131-2 (UL 508) when installed in the enclosure to prevent high | AC driv | e, recorder, | | | nnect 🔄 terminal of power | #26 | H 404C | . 0 | R/W To | o adjust GAI |
| | temperature, high humidity, exceessive vibration, corrosive gases, liquids, airbome dust or | scale v | e, recorder, 🛓 shielding cable *1 | | module a | nd 🔄 terminal of analog output | #27 | H 404D | , 0 | | alue of CH3 o adjust GAII |
| | metallic particles. Also, it is equipped with protective methods such as some special tool or key to open the enclosure, in order to prevent the hazard to users and damage DVP04DA-H. | termina norman | al of the second s | ÷+ ++15\ | module to | system earth point and make | #29 | 3~#29 | | | alue of CH4 |
| Â | Do not connect the AC power to any of the input/output terminals, or it may damage to the | power | *3 DC24V + 24+ system grounding | DC/DC +15V | system ea | arth point be grounding or | #30 | H 4050 | | | Frror status |
| | DVP04DA-H. Make sure that all the wiring is well conducted prior to power on. | | | | connects | to machine cover. | #31 | H 4051 | \circ | | Communication Iddress settin |
| A | Do not touch the internal circuit for at least 1 minute after the power supply is OFF. | | class 3 grounding (100 O or less) | | Warning: DO NO | wire to the No function terminal | #32 | H 4052 | 0 | R/W Co | Communication |
| A | Make sure that the DVP04DA-H is properly grounded 🕒, to prevent any electromagnetic noise. | | | | ●. | | | | | De | auu itale sei |
| 2 | INTRODUCTION | 3 | | STANDARD | SPECIFICATIO | NS | | | | | |
| | | | On a sifi s sti s s s | | | | | | | | |
| 2.1 | Model Explanation and Peripherals | 3.1 Digi | Specifications tal/Analog (4D/A) Module | Voltag | e Output | Current Output | | | | | |
| | Thank you for choosing DELTA's PLC DVP Series. The analog output module of DVP04DA-H | | r Supply Voltage | | C~28.8VDC) (–15% | | #33 | H 4053 | 0 | | Reset to facto etting and se |
| | series can read/write the data of analog output module by using instructions FROM / TO via | - | | | | 5~+20 <i>%</i>) | | | | ch | haracteristics |
| | DVP-PLC EH Series MPU program. The analog output module receives 12-bit digital data of 4 | | g Output Channel | 4 channels / eac | n module | | | | | ac | djustable pric |
| | groups from PLC MPU and converts it into 4 points analog output signal either in voltage or in | - | g Output Range | 0~10V | | 0~20 mA | | | | | |
| | current. | - | I Data Range | 0~4000 | | 0~4000 | | | | | |
| ٠ | Software version of DVP04DA-H analog output module can be updated via RS-485. | Resol | ution | 12 bits (1 _{LSB} =2.5 | mV) | 12 bits (1 _{LSB} =5 μA) | #0.4 | 11 4054 | | | - 4 |
| ٠ | Users can select output from voltage or current via wiring. Voltage output range is $0V \sim +10V$ DC | Outpu | it Impedance | 0.5Ω or lower | | | #34 #35 | H 4054 ~#48 | | | Software versi System used |
| | (resolution is 2.5 mV). Current output range is 0mA ~ 20mA (resolution is 5 µA). | Overa | III Accuracy | | ale of 25° C (77°F) | | | - | - | s latched. s not latcl | |
| | Nameplate Explanation | 0.010 | | ±1% of full scale | during 0~55℃ (32~ | ~131°F) | | Ŕ | R means | can read | d data by usi |
| | | Resp | onse Time | 3 ms ×channels | | | | | | | te data by us ficant Bit): 1 |
| | ANELTA Programmable Controller | Max. | Output Current | 20 mA(1KΩ~2M | Ω) | — | | anation | | 00/0 | |
| | Model Name MODEL : DVP04DA-H | Tolera | ince Carried Impedance | - | | 0~500 Ω | | ne conte odule. | ent of | CR#01 | is model |
| | Input power supply spec. Power INPUT : 20.4VDC ~ 28.8VDC ANALOG I/O RANGE : 0 ~ +10V or -0 ~ +20mA | Digita | I Data Format | 2's complementa | ary of 16-bit, 13 Sign | ificant Bits | | | used f | to set f | two inner |
| | Analog input/output module spec. RESOLUTION: 2.5mV or 5μA | Isolat | on Method | | | a and analog area. There is no | | | | | id can be |
| | Barcode VX.XX 04DA-H0T4130001 | | | isolation among | | ection but short circuit for a long | | | | | 3=001). It 0 ~ CR#1 |
| | DELTA ELECTRONICS, INC. MADE IN XXXXXX | Prote | ction | | | her wiring damage and current | 4. CF | R #6 ~ (| CR#9 | display | y CH1~C |
| _ | Madel Freedow Afra | | | output break. | | 5 5 | | nd unit i | | | ans the va |
| - | Model Explanation Model Serial Number | | | | | ommunication rate can be 4800 200. Communication format of | | | | | utput valu |
| | | Comr | nunication mode (RS-485) | | | op bit (7 E 1). Communication | VC | oltage o | r curre | ent is -2 | 2000~+20 |
| | | | | format of RTU n | node is 8Bit, even b | pit, 1 stop bit (8 E 1). Can't use | | | | | nge: -5V~ nge: -10m |
| | Product Series • Production week | | | | IPU connection is in | series. cted to MPU. the modules are | | | | | ans the va |
| | Input+Output point • S: for SS series MPU P: for EP series MPU H: for EH series MPU | Conn | ect to DVP-PLC MPU in | - | | sest and 7 is the furthest to the | | | | | t value e |
| | Model type | series | ; | | | ey do not occupy any digital I/O | | | | | 1600~+80 nge: -4V~ |
| | AD: Analog input module XA: A/D , D/A Functions Serial number of version Production Model | | | points of the MP | U. | | | | | | nge: -8mA |
| | PT: Platinum temperature sensors(PT-100) HC: High speed count input module TC: Thermocouple sensors(Type J/K) PU: single axis positioning unit | 3.2 | Other Specification | | | | | | | | t GAIN VA |
| 2.2 | Product Profile and Outline | | | Power Specificat | | | | | | | ies up sn larger. O |
| | | Max. | Rated Consuming Power | external power | 28.8VDC) (-15% | \sim +20%), 2W, supply from | sig | gnal res | solutior | n becor | mes large |
| | | | | Environment Cond | dition | | | | | | Please ref |
| | | Enviro | onment Condition | Follow the DVP-I | | | | ult Desover So | | | nal ^k |
| | | | of Prevent Static | All places betwee | en terminals and arc | ound comply with the spec | | alog In | | | |
| (| | Electr | ICITY | | | | | etting Mo | | | ĸ |
| | | 4 | | CR (Co | ntrol Register) | | | fset/Gai ardware | | | |
| | | | /P04DA-H Analog Output Mod | dule | Fynla | anation | | gital Ra | | | |
| | | CR | RS-485 | | | | Av | erage T | Times | Setting | J Error 🛛 🖌 |
| | | INO. | arameters Latched Register | | b12 b11 b10 b9 b8 | | | struction | | | K |
| | | #0 #1 | H 4032 O R Model type H 4033 O R/W Output mod | | VP04DA-H model code=H CH4 | 1 0401 CH3 CH2 CH1 | No | | | | ill have cori d 1 means l |
| | | | | Output mode se | etting: factory setting is H0 ut voltage mode (0V~10V) | 000. | 8. C | | | | RS-485 c |
| | | | | Mode 1: outp | ut voltage mode (2V~10V) | | se | etting is | K1. | | |
| | | | | Mode 3: outp | ut current mode (4mA~20r ut current mode (0mA~20r | | | | | | |
| | Unit:mm | 1 1 | | Mode 4: none | use. | | | | | | DO 105 |

Mode 4: none use

Reserved

#2 ~ ±

6. Terminals

| lodule | Explanation | | | | | | | | | | | | | | | |
|--|--|---|--------|--------|------|------------------|--------|-------|-------|--------|----------|---------|--------|---------|--------|------|
| er Name | b15 | b14 | b13 | b12 | b11 | b10 | b9 | b8 | b7 | b6 | b5 | b4 | b3 | b2 | b1 | b0 |
| ut value ut value ut value ut value | | The output setting range of channel CH1~CH4 is K0~K4000. Factory setting is K0 and unit is LSB. | | | | | | | | | | | | | | |
| | Res | erved | | | | | | | | | | | | | | |
| OFFSET CH1 OFFSET CH2 OFFSET CH3 OFFSET CH4 | It is | used | to set | | | ET va ry sett | | | | | | g ran | ge is | | | |
| | Res | erved | | | | | | | | | | | | | | |
| GAIN CH1 GAIN CH2 GAIN CH3 GAIN CH4 | | | | | | value 00 and | | | | ie set | ting ra | ange i | s K-1 | 600~ł | <8000 | l. |
| | Res | erved | | | | | | | | | | | | | | |
| us | | | | sed to | save | all er | ror st | atus. | Pleas | e refe | er to fa | ault co | ode ch | nart fo | r deta | ils. |
| ication setting ication e setting | It is used to set RS-485 communication address. The setting range is from 01 to 255 and the factory setting is K1. It is used to set communication baud rate (4800, 9600, 19200, 38400, 57600, 115200bps). Communication format: ASCII mode is 7Bit, even bit, 1 stop bit (7 E 1). Communication format of RTU mode is 8Bit, even bit, 1 stop bit (8 E 1). | | | | | | | | | | | | | | | |
| | b0: 4800 bps (bit/sec). b1: 9600 bps (bit/sec). (factory setting) b2: 19200 bps (bit/sec). b3: 38400 bps (bit/sec). b4: 57600 bps (bit/sec). b5: 115200 bps (bit/sec). b6-b13: reserved. b14: exchange low and high byte of CRC check code (only for RTU mode) b15: ASCII / RTU mode selection | | | | | | | | | | | | | | | |
| factory | b15 | b14 | b13 | b12 | b11 | b10 | b9 | b8 | b7 | b6 | b5 | b4 | b3 | b2 | b1 | b0 |
| nd set istics e priority | Bit Inform Bit Inform | | | | | | | | | | | | | | | |
| version. | | | | | | y soft | | | | | | |)A me | ans 1 | .0A. | |
| sed | | - | | | | | | - | - | | | | - | | | |
| | | | | | | | | | | | | | | | | |

lata by using FROM instruction or RS-485.

DVP04DA-H Analog Output Mo

data by using TO instruction or RS-485. ant Bit): 1. Voltage output: 1_{LSB}=10V/8000=2.5mV. 2. Current output: 1_{LSB}=20mA/4000=5µ/

model type, user can read the data from program to know if there is extension

vo inner channels working mode of analog output module. Every channel has can be set individually. For example: if setting CH1 to mode 2 (b2~b0=010). =001). It needs to set CR#1 to H000A. The factory setting of CR#1 is H0000.

CR#17, CR#22, CR#23, CR#28, CR#29 Reserved. CH1~CH4 output signal. The setting range is K0~K4000. Factory setting is K0

is the value of adjusting OFFSET value of CH1~CH4. The factory setting is K0 put value equal to 0 after calculating, the adjustable range of analog output . 00~+2000.

ge: -5V~+5V(-2000_{LSB}~+2000_{LSB}).

e: -10mA~+10mA (-2000_{LSB}~+2000_{LSB}).

the value of adjust GAIN value of CH1~CH4. The factory setting is K2000 and value equal to 2000 after calculating, the adjustable range of analog output 300~+8000

e: -4V~+20V(-1600_{LSB}~+8000_{LSB}).

e: -8mA ~+40mA (-1600_{LSB}~+8000_{LSB}).

GAIN VALUE – OFFSET VALUE = +400_{LSB} ~+6000_{LSB} (voltage or current). If the up small (within range), the output signal resolution is then slim and the rger. On the contrast, if the value difference exceeds the range, the output es larger and the variation is definitely smaller ease refer to the following ch

| refer to the | tollowing | chart. | | | | | | | |
|--------------|-------------|--------|----|-------------|----|----|----|----|----|
| Content | b15~b8 | b7 | b6 | b5 | b4 | b3 | b2 | b1 | b0 |
| K1(H1) | | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 |
| K2(H2) | | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 |
| K4(H4) | | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 |
| K8(H8) | Reserved | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 |
| K16(H10) | Reserved | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 |
| K32(H20) | | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 |
| K64(H40) | | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 |
| K128(H80) | | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | 1 11 /1 0 1 | | | <i>c</i> 11 | | | | | |

have corresponding bit (b0~b7). Two or more faults may happen at the same time. (means having fault.

S-485 communication address. The setting range is from 01 to 255. The factory

bps. b0: 4800bps. b1: 9600bps. (factory setting) b2: 19200bps. b3: 38400 bps. b4: 57600 bps. b5: 115200 bps. b6-b13: reserved. b14: exchange low and high byte of CRC check code. (only for RTU mode) b15=0: ASCII mode, b15=1: RTU mode, Communication format: ASCII mode is 7Bit, even bit, 1 stop bit (7 E 1), while RTU mode is 8Bit, even bit, 1 stop bit (8 E 1).

- 10. CR#33 is used to set the internal function priority, such as characteristic register. Output latched function will save output setting in the internal memory before power loss.
- 11. CR#34 is software version of model type.
- 12. CR#35~ CR#48 are used for system.
- 13. The corresponding parameters address H4032~H4063 of CR#0~CR#48 are provided for user to read/write data through RS-485.
 - Communication baud rate: 4800, 9600, 19200, 38400, 57600, 115200 bps. Α
 - Communication format: ASCII mode is 7Bit, even bit, 1 stop bit (7 E 1), while RTU mode is Β. 8Bit, even bit, 1 stop bit (8 E 1).
 - C. Function code: 03H-read data from register. 06H-write one WORD into register. 10H-write multiple WORD into register.

ADJUST D/A CONVERSION CHARACTERISTIC CURVE

5.1 Adjust D/A Conversion Characteristic Curve

+4000

K1

K33

K18

K24

K1

K33

K19

K25

(CR#18~CR#21) and GAIN values (CR#24~CR#27) depend on application.

+2000

1_{LSB}=20mA/4000=5µA.

ТО

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то

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M1002

—| |-

XO

-|↑|

M1002

X0

-I ♠I

digital input

K1

K1

K1

K1

K1

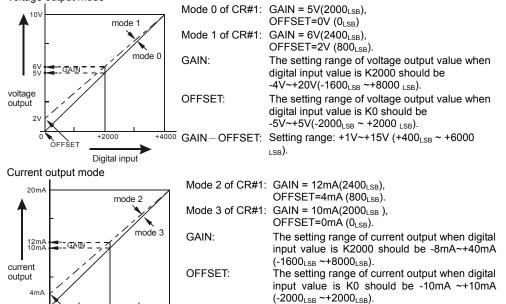
K1

K1

K1

Voltage output mode

5



output mode. Users can adjust conversion characteristic curve by changing OFFSET values

LSB(Least Significant Bit): 1.voltage output:1LSB=10V/4000=2.5mV. 2. current output:

Example 1: Setting OFFSET value of CH1 to 0V(=K0_{LSB}) and GAIN value is 2.5V(=K1000_{LSB}).

H10

H0

K0

H18

K3600

5.2 Program Example for Adjusting D/A Conversion Characteristics Curve

K1

K1

K1

K1

K1

GAIN-OFFSET: Setting range: +2mA~+30mA (+400_{LSB}

~+6000_{LSB}).

Explanation⁻

6

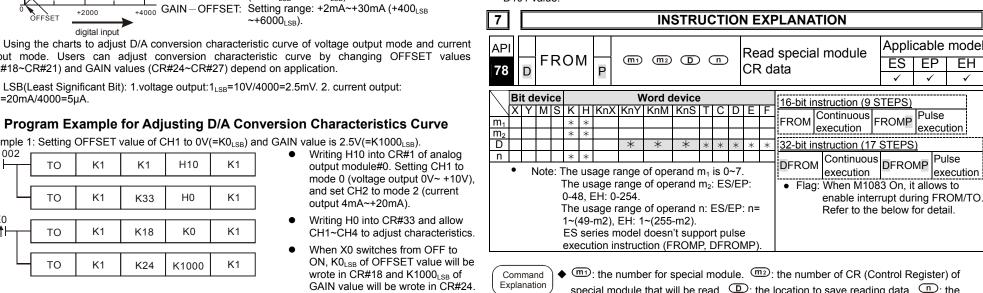
Lamp display

Program example:

- Reading the data of model type from extension module K1 and distinguish if the data is H0401 (DVP04DA-H model type).
- D100 will increase K1 and D101 will increase K5 every second.
- When value of D100 and D101 gets to K4000, they will be reset to 0.
- If the model type is DVP04DA-H, M1 will be on and set the output mode: CH1 mode to 0, CH2 mode to 2

INITIAL PLC START-UP

• Writing output setting CR#6 and CR#7 to D100 and D101. Analog output will change with D100 and D101 value.



special module that will be read. D: the location to save reading data. C: the data number of reading ONCE.

- DVP-series PLC uses this instruction to read CR data of special module.
- D: When assigning bit operand, K1~K4 are used for 16-bit and K5~K8 are used for 32-bit
- Please refer the footnote below for the calculation of special module number.
- ◆ To read the content of CR#24 of special module#0 to D0 of PLC and to read the Program Example content of CR#25 of special module#0 to D1 of PLC. It can read 2 data in one time (n=2)
 - ◆ The instruction will be executed when X0=ON. The instruction won't be executed when X0=OFF and the content of previous reading data won't change

| FROM K0 K24 D0 K2 | | • | | | | • |
|-------------------|------|------|----|-----|----|----|
| | ⊢ĩ⊢– | FROM | K0 | K24 | D0 | K2 |

| mp display When power is on, POWER LED will be lit and ERROR LED will be lit for 0.5 second. | 79 D TO P |
|--|--|
| Normal run: POWER LED should be lit and ERROR LED should turn off. When power supply is lower than 19.5V, ERROR LED will blink continuously till the power supply goes higher than 19.5V. When it connects to PLC MPU in series, RUN LED on MPU will be lit and A/D LED or D/A LED should blink. | Bit device X Y M S K H KnX K m1 * * m2 * * S * * * |
| After receiving the first RS-485 instruction during controlling by RS-485, A/D LED or D/A LED should blink. After converting, ERROR LED should blink if input or output exceeds the upper bound or below than lower bound. | Note: The usage rang 0 -48, EH: 0-254 |
| ogram example: | The usage rang 1~(49-m2), EH: |
| FROM K1 K0 D0 K1 | For ES series, execution instru |
| CMP H401 D0 M0 | Command • (m1): the num |
| | Explanation special modul number to wr |
| ADD D101 K5 D101 | DVP-series F S: When a |
| LD= K4000 D100 RST D100 | used for 32-b |
| LD= K4000 D101 RST D101 | Program Example ♦ Using 32-bit is special modu The instruction X0=OFF. The |
| - ITO K1 K1 H10 K1 | - |
| ТО K1 K6 D100 K2 | Footnote Footnote |
| nation: ading the data of model type from extension module K1 and distinguish if the data is H0401 /P04DA-H model type). 00 will increase K1 and D101 will increase K5 every second. en value of D100 and D101 gets to K4000, they will be reset to 0. he model type is DVP04DA-H, M1 will be on and set the output mode: CH1 mode to 0, CH2 mode | near and v m2: t is cal (#0~# incluc • If usin |
| ting output setting CR#6 and CR#7 to D100 and D101. Analog output will change with D100 and D1 value. | one ti numb |
| | ı [|
| D FROM min min <td>(Access1</td> | (Access1 |
| it device Word device Y M S K H KnX KnY KnM KnS T C D E F - - - <td>D0 D1 D2 D3 D4 D5</td> | D0 D1 D2 D3 D4 D5 |
| 0-48, EH: 0-254. The usage range of operand n: ES/EP: n= 1~(49-m2), EH: 1~(255-m2). ES series model doesn't support pulse execution instruction (FROMP, DFROMP). | 16-bi ♦ In ES serie executed, a disabled. A Besides, FF ♦ The function |
| $mand \rightarrow (m_1)$: the number for special module. (m_2) : the number of CR (Control Register) of | The function |

API

- series models

H0 K1 K400 K1

and set CH2 to mode 2 (current output 4mA~+20mA). • Writing H0 into CR#33 and allow CH1~CH4 to adjust characteristics. • When X0 switches from OFF to

Writing H10 into CR#1 of analog

output module#0. Setting CH1 to

ON, K0_{LSB} of OFFSET value will be wrote in CR#18 and K1000_{LSB} of

Example 2: Setting OFFSET value of CH2 to 2mA (=K400 LSB) and GAIN value to 18 mA (=K3600 LSB).

- K1000 GAIN value will be wrote in CR#24. • Writing H18 into CR#1 of analog K1 output module#0. Setting CH1 to mode 0 (voltage output 0V~+10V)
 - and set CH2 to mode 3 (current output 0mA~ +20mA).
 - Writing H0 into CR#33 and allow to adjust characteristic of CH1~CH4. When X0 switches from OFF to
 - ON, K400_{LSB} of OFFSET value will be wrote in CR#19 and K3600_{LSB} of GAIN value will be wrote in CR#25.

| | | | | | | | | lule CR | Applicable model | | | | | | |
|---|---|---|--|--|--|---|---|--|---|---|--|--|--|--|--|
| (m ₂) | S | | <u>n</u> | , , | | | | | ES | EP | EH | | | | |
| | | | | | | | | | \checkmark | \checkmark | \checkmark | | | | |
| ord d | evice | | | | | | 16-bit ir | nstruction (9 S | TEPS) | | 7 | | | | |
| INY KnM KnS T C D E F | | | | | | | | TO Continuous TOP Pulse | | | | | | | |
| | | | | | | | | execution | | exec | cution | | | | |
| * * * * * * * * | | | | | | | | 32-bit instruction (17 STEPS) | | | | | | | |
| | | | | | | | | | | | | | | | |
| e of operand m₁ is 0~7. | | | | | | | | | | exec | cution | | | | |
| ge of operand m ₂ : ES/EP: 4. | | | | | | | Flag: When M1083=On, it allows to insert interrupt during FROM/TO. | | | | | | | | |
| ge of operand n: ES/EP: : 1~(255-m2). | | | | | | | Refer to following for detail. | | | | | | | | |
| | | | - - - | llse | ! | | | | | | | | | | |
| | KnM * f opera f opera f opera (255-1 oesn't | ord device KnM KnS * * f operand n f operand r f operand r (255-m2). oesn't supp | ord device KnM KnS T * * * f operand m1 f operand m2: f operand n: E (255-m2). oesn't support | ord device KnM KnS T C * | ord device KnM KnS T C D $*$ | f operand m_2 : ES/EP: (255-m2). oesn't support pulse | data v ord device KnM KnS T C D E F * * * * * * * * f operand m ₁ is 0~7. f operand m ₂ : ES/EP: f operand n: ES/EP: (255-m2). oesn't support pulse | data write in data write in $\overline{KnM[KnS]T[C]D]E[F]}$ $\overline{KnM[KnS]T[C]D]E[F]}$ \overline{TO} $\underline{KnM[KnS]T[C]D]E[F]}$ \overline{TO} $\underline{KnM[KnS]T[C]D]E[F]}$ \overline{TO} $\underline{S2-bit ir}$ \overline{TO} $\underline{S2-bit ir}$ \overline{DTO} $\underline{S2-bit ir}$ \overline{DTO} $\underline{S2-bit ir}$ \overline{DTO} $\underline{S2-bit ir}$ \overline{DTO} $\underline{S2-bit ir}$ \overline{DTO} $\underline{S2-bit ir}$ \overline{DTO} $\underline{S2-bit ir}$ \overline{DTO} $\underline{S2-bit ir}$ \overline{DTO} $\underline{S2-bit ir}$ $\underline{S2-bit ir}$ S2-b | ord device Index write in KnM KnS T C D E F Continuous KnM KnS T C D E F Continuous TO Continuous * * * * * * * * 32-bit instruction (17 S f operand m1 is 0~7. Continuous f operand m2: ES/EP: Continuous f operand n: ES/EP: Flag: When M108: insert interru Refer to follo cosn't support pulse Refer to follo | Implication Special module CR data write in Special module CR data write in ES Implication Implication Implication Ord device Implication Implication Implication KnM KnS T C D E F Implication Implication Implication Implication KnM KnS T C D E F Implication Implication Implication Implication Implication KnM KnS T C D E F Implication Implication Implication Implication Implication Implication KnM KnS T C D E F Implication Implication | (m2) (S) (n)Special module CR data write inimode ESord deviceIf 6-bit instruction (9 STEPS)KnM KnS T C D E FTOKnM KnS T C D E FTOContinuousTOPPulsew * * * * * * *Image: Special module CRthe special module CRKnM KnS T C D E FTOContinuousTOContinuousTOPPulseProperand m1 is 0~7.f operand m2: ES/EP:f operand n: ES/EP:(255-m2).oesn't support pulse | | | | |

mber of special module. (m2): the number of CR (Control Register) of ule that will be wrote in. (S): the data to write in CR. (n): the data rite in one time

PLC uses this instruction to write data into CR of special module. assigning bit operand, K1~K4 can be used for 16-bit and K5~K8 can be bit

instruction DTO, program will write D11 and D10 into CR#3 and CR#2 of ule#0. It only writes a group of data in one time (n=1).

on will be executed when X0=ON and it won't be executed when he data that wrote in previous won't have any change.

| | | DTO | K0 | K2 | D0 | K1 |
|--|--|-----|----|----|----|----|
|--|--|-----|----|----|----|----|

nstruction operand

arrangement number of special module. The number of special module connects to PLC MPU. The numbering order of special module from the to the distant of MPU is from 0 to 7. The maximum is 8 special modules won't occupy I/O point

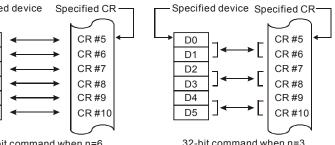
the number of CR. Built-in 16-bit of 49 groups memory of special module Iled CR (Control Register). The number of CR uses decimal digital #48). All running status and setting values of special module have ded

ing FROM/TO instruction, the unit of read/write of CR is one number for time. If using DFROM/DTO instruction, the unit of read/write of CR is two bers in one time.

Upper 16-bit Lower 16-bit

CR #10 CR #9 Specified CR number

16-bit if n=2, or 32-bit if n=1. Same controlled registers are accessed).



it command when n=6

32-bit command when n=3

ies models, flag M1083 is not provided. When FROM/TO instruction is all interrupts (including external or internal interrupt subroutines) will be All interrupts will be executed after completing FROM/TO instruction. ROM/TO instruction also can be executed in the interrupt subroutine.

on of the flag M1083 (FROM/TO mode exchange) provided in EP/EH

1. When M1083=Off, all interrupts (including external or internal interrupt subroutines) will be disabled when FROM/TO instruction is executed. The Interrupts will resumed after FROM/TO instruction complete. Please be advised FROM/TO instruction can be executed in the interrupt subroutine. 2. When M1083=On, if an interrupt enable occurs while FROM/TO instruction are executing, the interrupt FROM/TO instruction will be blocked till the requested interrupt finish. Unlike M1080 off situation, FROM/TO instruction cannot be executed in the interrupt subroutine.